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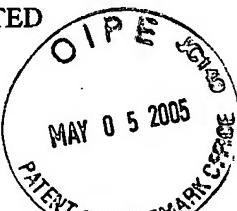
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23494 7590 04/19/2005

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Karen Vertz	(Depositor's name)
<i>Karen Vertz</i>	(Signature)
5-5-05	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/725,642	12/02/2003	Taylor R. Efland	TI-32451	4541

TITLE OF INVENTION: LOW COST FABRICATION METHOD FOR HIGH VOLTAGE, HIGH DRAIN CURRENT MOS TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/19/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
PERT, EVAN T	2826	438-286000

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- W. James Brady, III
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Texas Instruments Incorporated

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Dallas, Texas | 01 FC:1501 1400.00 DA
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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Gary C. Honeycutt

Typed or printed name

Gary C. Honeycutt

Date 5 May '05

Registration No. 20,250

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of

Applicant Taylor R. Efland, et al.

Docket Number: TI-32451

Serial No.: 10/725,642

Art Unit: 2826

Filed: 12/02/03

Examiner: Evan T. Pert

For: Low Cost Fabrication Method for High Voltage, High Drain
Current MOS Transistor

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NAME OF INVENTOR(S):	
Taylor R. Efland, et al.	
TITLE OF INVENTION: Low Cost Fabrication Method for High Voltage, High Drain Current MOS Transistor	
TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-32451	20-0668
FAXED: 5-6-05	
DUE: 7-19-05	
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